CLAIMS

I claim:

1. A method, comprising:

forming a die with a surface;

forming conductive bumps on the surface of the die, the conductive bumps having a height equal or greater than the height of a waveguide;

forming a substrate; and

bonding the conductive bumps to the substrate.

- 2. The method of claim 1, wherein the waveguide has a height in a range of about 95 micrometers to about 110 micrometers.
- 3. The method of claim 1, wherein the conductive bumps have a height greater than about 80 micrometers.
- 4. The method of claim 1, wherein the conductive bumps have a height in a range from 80 micrometers to about 120 micrometers.
- 5. The method of claim 1, wherein the conductive bumps have a height in a range from 95 micrometers to about 110 micrometers.
- 6. The method of claim 1, wherein forming conductive bumps comprises:

depositing a first conductive layer on the die;

depositing a mask material layer on the thin conductive layer;

patterning the mask material layer to form pad openings;

depositing a second conductive layer in the pad openings of the patterned mask; and

removing the mask material.

- 7. The method of claim 6, further comprising depositing a protection layer on the second conductive layer.
- 8. The method of claim 7, further comprising depositing a barrier layer between the protection layer and second conductive layer.
- 9. The method of claim 7, wherein bonding the conductive bumps to the substrate comprises bonding the conductive bumps to the substrate with a fluxless soldering process.
- 10. The method of claim 1, wherein the conductive bumps are formed on a plurality of dies that are part of a wafer.
- 11. The method of claim 10, further comprising singulating the die from the wafer after forming the conductive bumps.
- 12. The method of claim 1, wherein the conductive bumps are bonded to the substrate at a bonding temperature and the conductive bumps have a melting point higher than the bonding temperature.
- 13. The method of claim 12, wherein the bonding temperature is at least a melting point of a solder material that bonds the conductive bumps to the substrate.
- 14. The method of claim 13, wherein the bonding temperature is about 230 degrees Celsius.

EV336583497US - 11 - 42P17278

15. A device, comprising:

a die;

a substrate separated from the die by a gap;

- a waveguide between the substrate and the die, the waveguide having a height; and
- a plurality of conductive bumps that extend from the die to the substrate and connect the die with the substrate, wherein the conductive bumps have a height equal to or greater than the height of the waveguide.
- 16. The device of claim 15, wherein the waveguide has a height in a range of about 95 micrometers to about 110 micrometers.
- 17. The device of claim 15, wherein the conductive bumps have a height greater than about 80 micrometers.
- 18. The device of claim 15, wherein the conductive bumps have a height in a range from 80 micrometers to about 120 micrometers.
- 19. The device of claim 15, wherein the conductive bumps have a height in a range from 95 micrometers to about 110 micrometers.
- 20. The device of claim 15, wherein the conductive bumps each comprise:

a first conductive layer;

a second conductive layer; and

a protection layer.

EV336583497US - 12 - 42P17278

- 21. The device of claim 20, wherein the first conductive layer comprises an adhesion layer and a seed layer.
- 22. The device of claim 21, wherein the adhesion layer comprises at least one of Ti, TiN, Cr, and Ta.
- 23. The device of claim 21, wherein the seed layer comprises at least one of Ni, NiV, Co, Cu, Au, and Ag.
- 24. The device of claim 20, wherein first conductive layer comprises at least one of Ti, TiN, Cr, Ta, Ni, NiV, Co, Cu, Au, and Ag.
- 25. The device of claim 20, wherein the second conductive layer comprises at least one of Cu, Ni, Co, Fe, Au, and Ag.
- 26. The device of claim 20, wherein the protection layer comprises at least one of Au, Pt, Pd, Ag, Ir, Os, Ru, and Rh.
- 27. A device, comprising:

a die;

- a substrate separated from the die by a gap;
- a waveguide between the substrate and the die, the waveguide not being located in a trench on the die or the substrate; and
- a plurality of bumps that extend from the die to the substrate and connect the die with the substrate.
- 28. The device of claim 27, wherein the waveguide has a height greater than approximately 90 micrometers.

EV336583497US - 13 - 42P17278

- 29. The device of claim 27, wherein the bumps have a height equal to or greater than the height of the waveguide.
- 30. The device of claim 27, wherein the bumps are formed by a method comprising:

depositing a first conductive layer on the die;

depositing a mask material layer on the thin conductive layer;

patterning the mask material layer to form pad openings;

depositing a second conductive layer in the trenches of the patterned mask; and

removing the mask material.

EV336583497US - 14 - 42P17278